

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application: Furukawa et al.

Serial No.: 09/944,665

Filed: 31 August 2001

Today's Date: 13 August 2002

Group Art Unit: 2811

Examiner: Loke, S.

Title: Vertical Dual Gate Field Effect Transistor

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AMENDMENT UNDER 35 U.S.C. 132

AUG 13 2002

To the Assistant Commissioner of Patents  
Washington, D.C. 20231

TECHNOLOGY CENTER 2800

Dear Sir:

In response to the Restriction Requirement of 1 August 2002, in which all of pending claims 1-44 are subject to restriction, Applicants elect to prosecute their embodiment of claims 18-44, with traverse. Applicants do not believe that any fee is due for the submission and consideration of this response; however, should the Patent Office determine that such a fee is due, Applicants hereby authorize the Patent Office to charge their Deposit Account 09-0456 such sums as may be due for the submission and consideration of this response.

## CERTIFICATE OF MAILING

I hereby certify that, on the date shown below, this correspondence is being:

## MAIL

☐ deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, DC 20231.

Date: 8/13/02

## FACSIMILE

☒ transmitted by facsimile to the Patent and Trademark Office.

Maryann Luisi

Name

Maryann Luisi

Signature

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